

FIG. 2

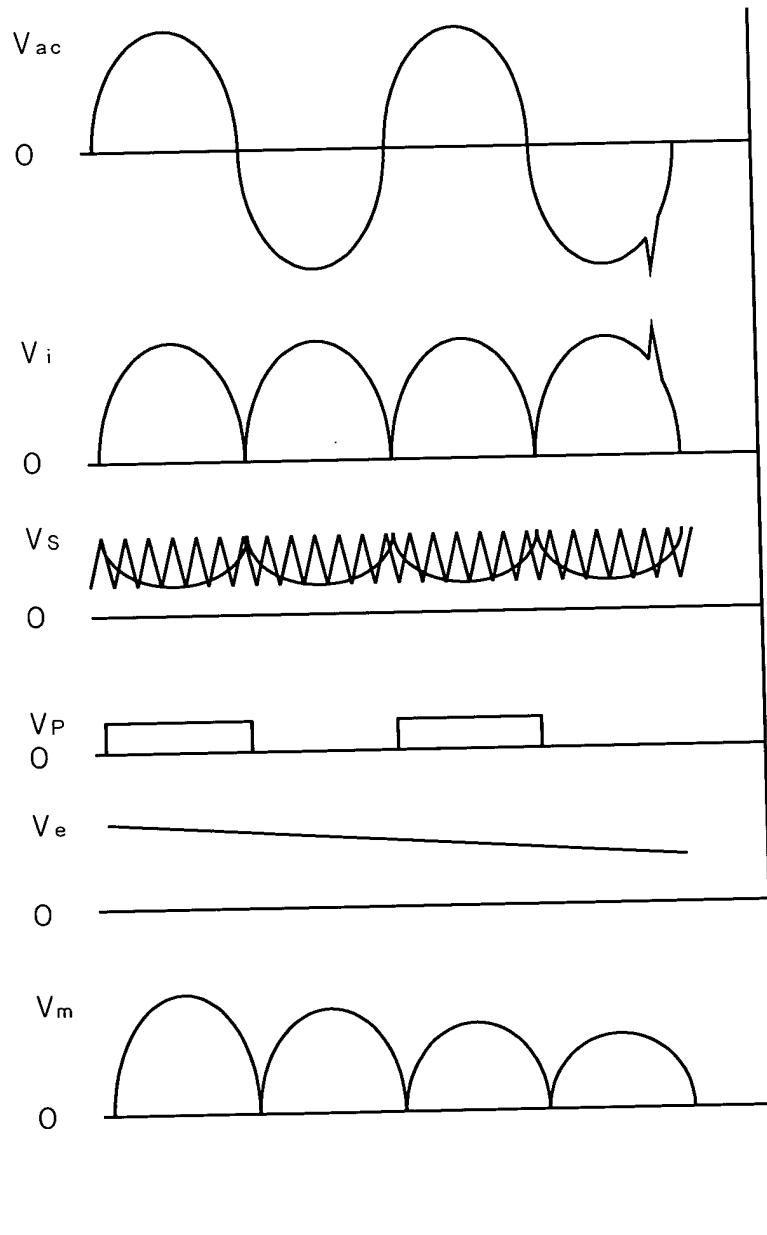


FIG. 3

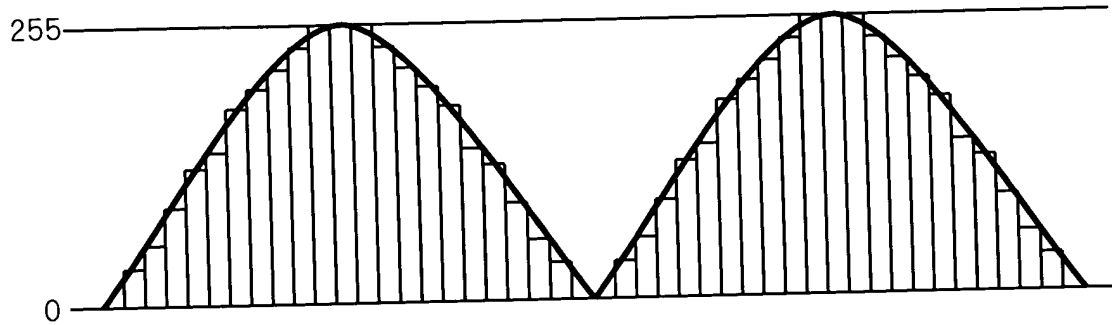


FIG. 4

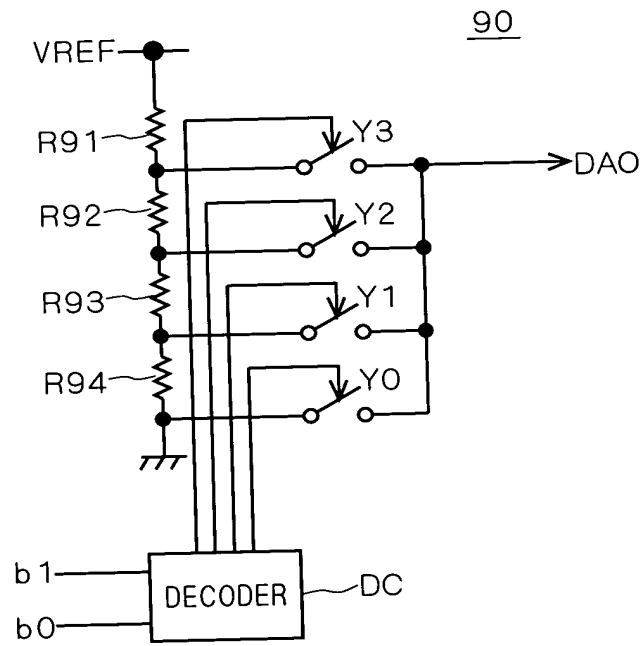
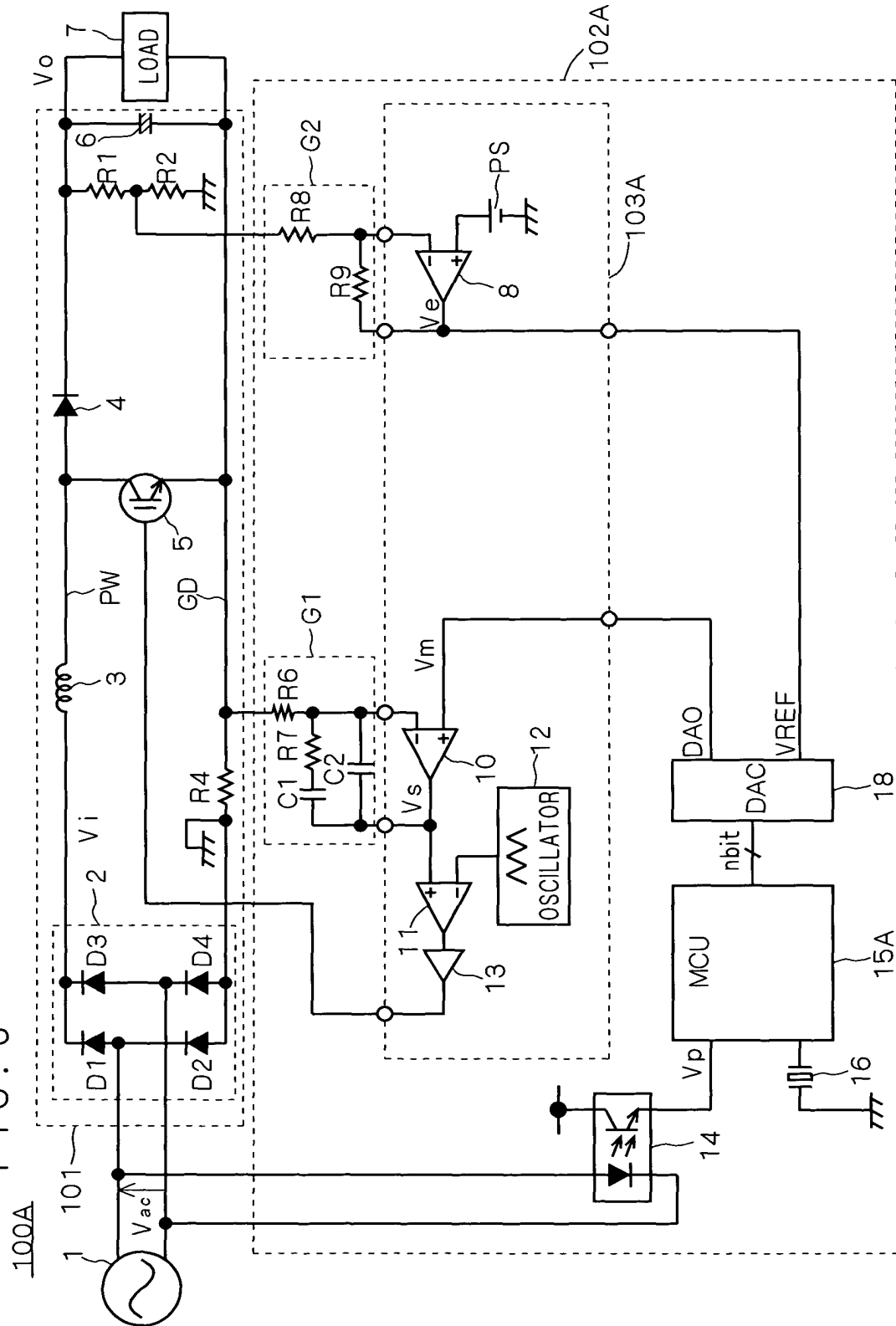
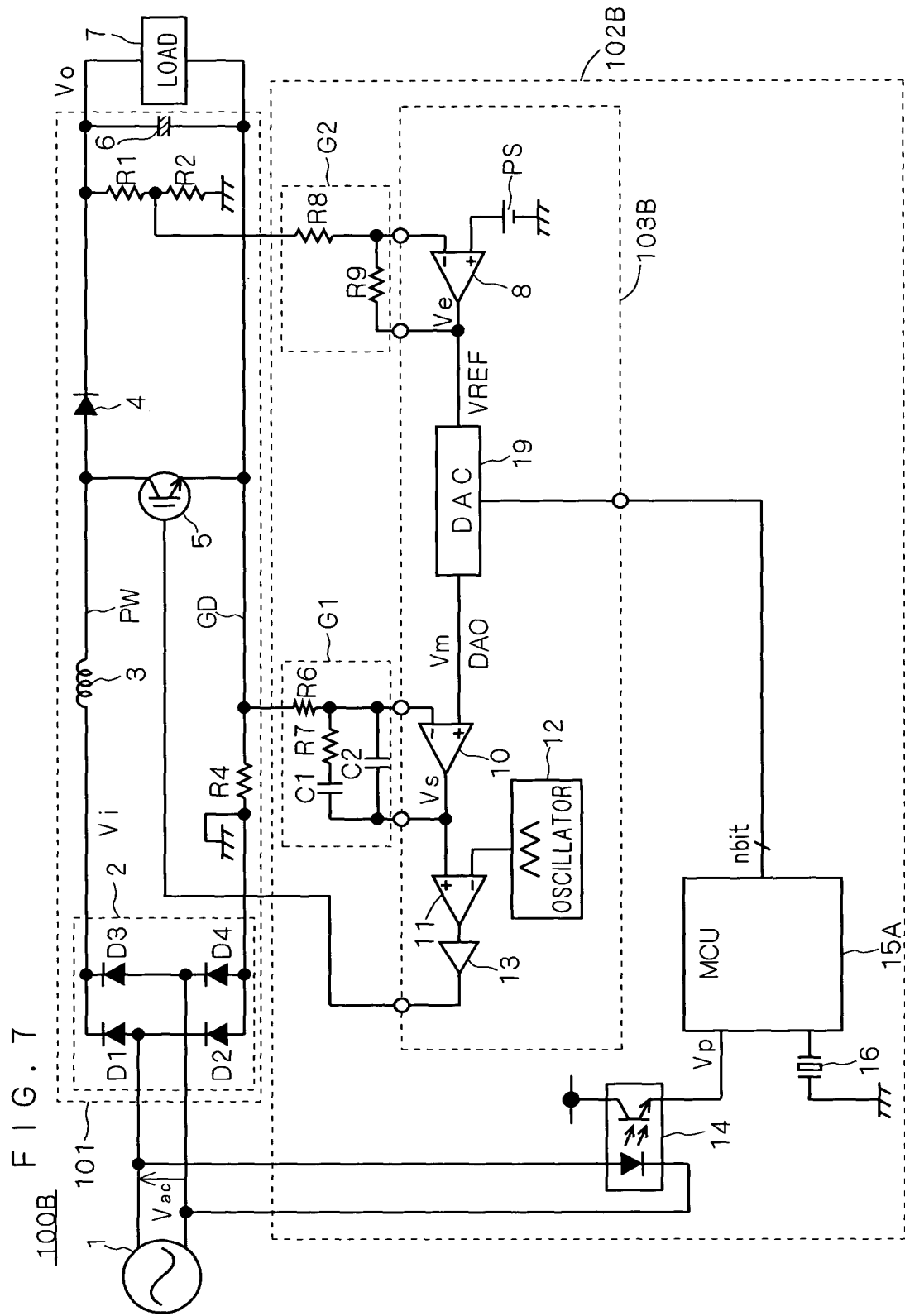


FIG. 5

| b 0 | b 1 | DECIMAL NUMBER | DAO             |
|-----|-----|----------------|-----------------|
| 0   | 0   | 0              | 0               |
| 1   | 0   | 1              | $(1/4) V_{REF}$ |
| 0   | 1   | 2              | $(2/4) V_{REF}$ |
| 1   | 1   | 3              | $(3/4) V_{REF}$ |

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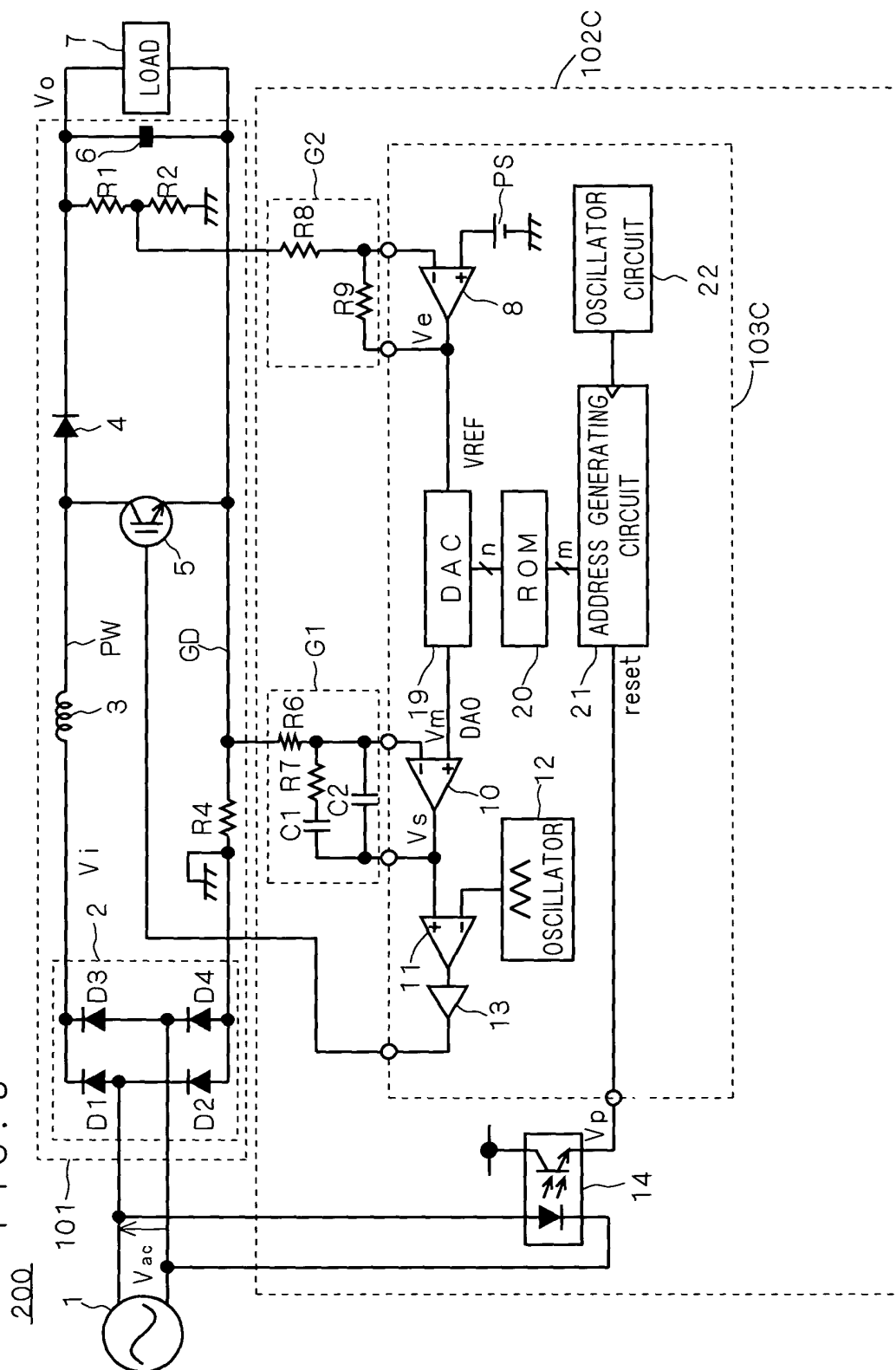


FIG. 9

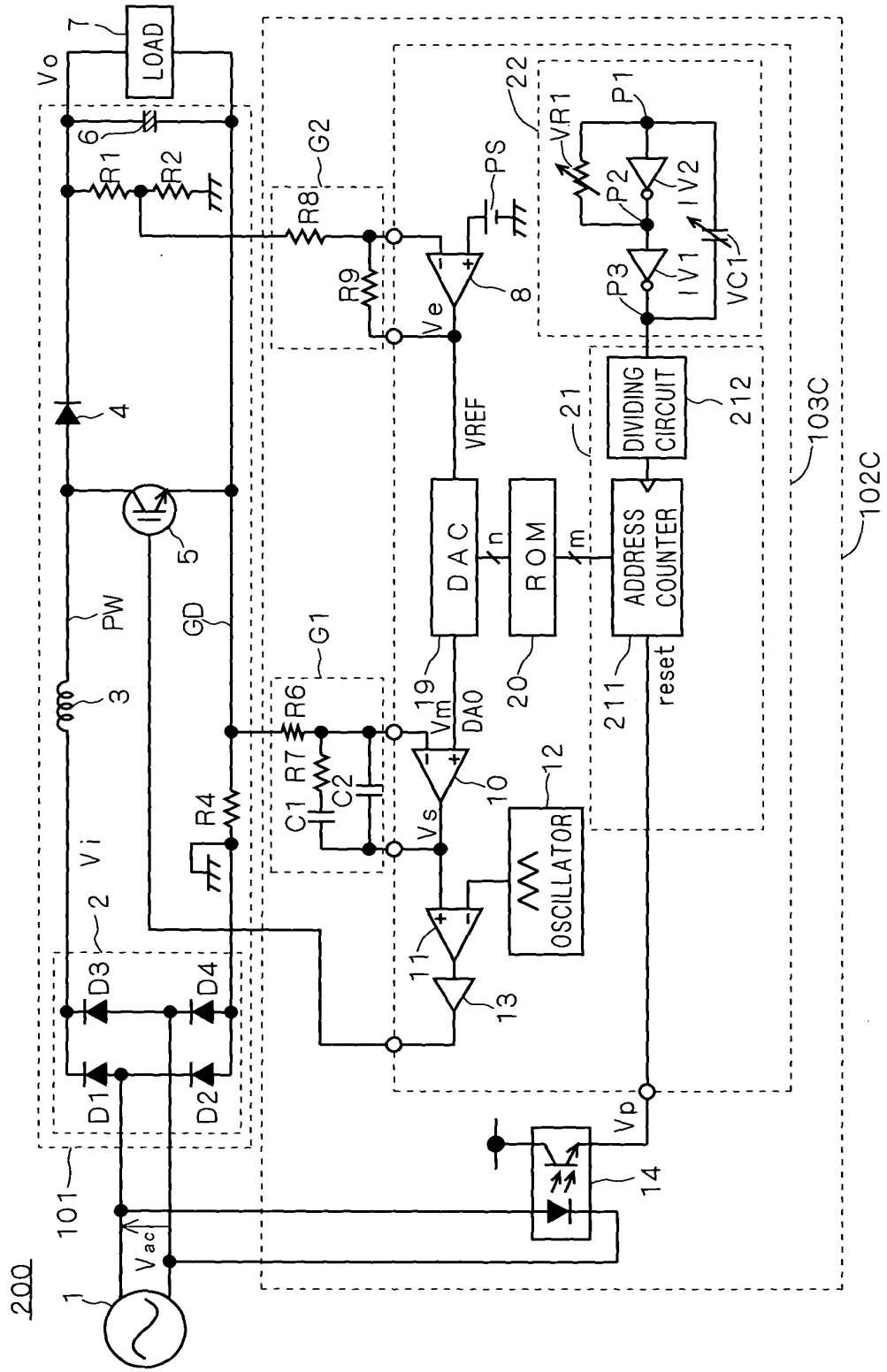


FIG. 10

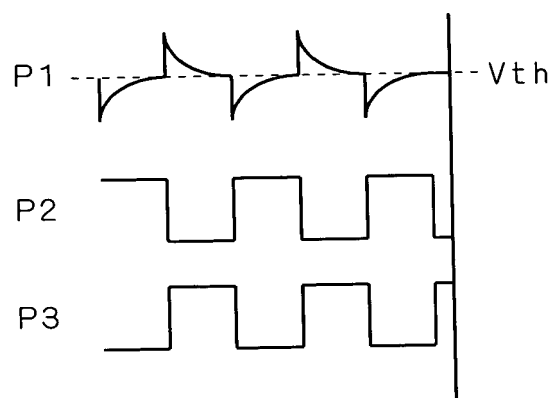
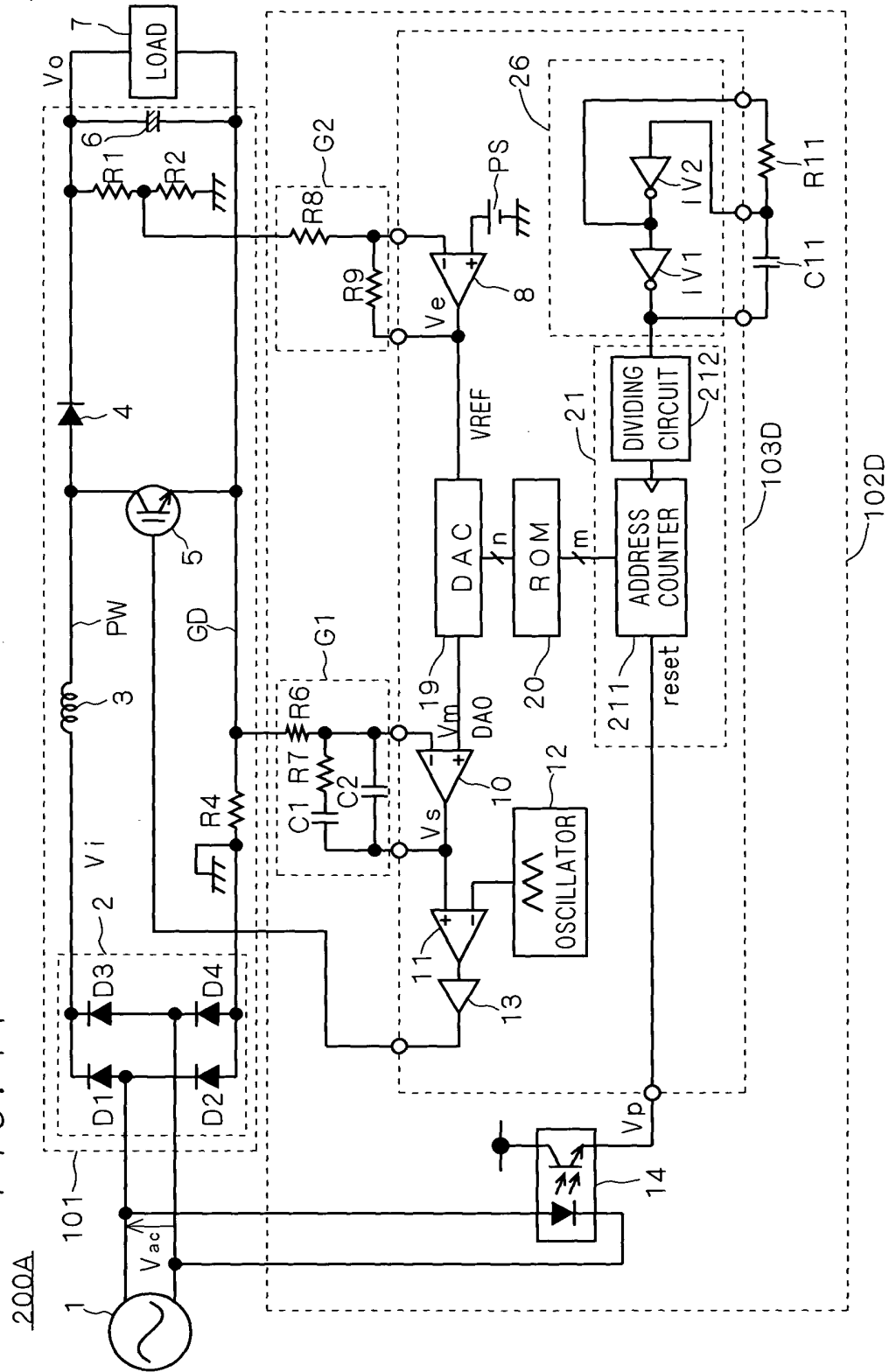
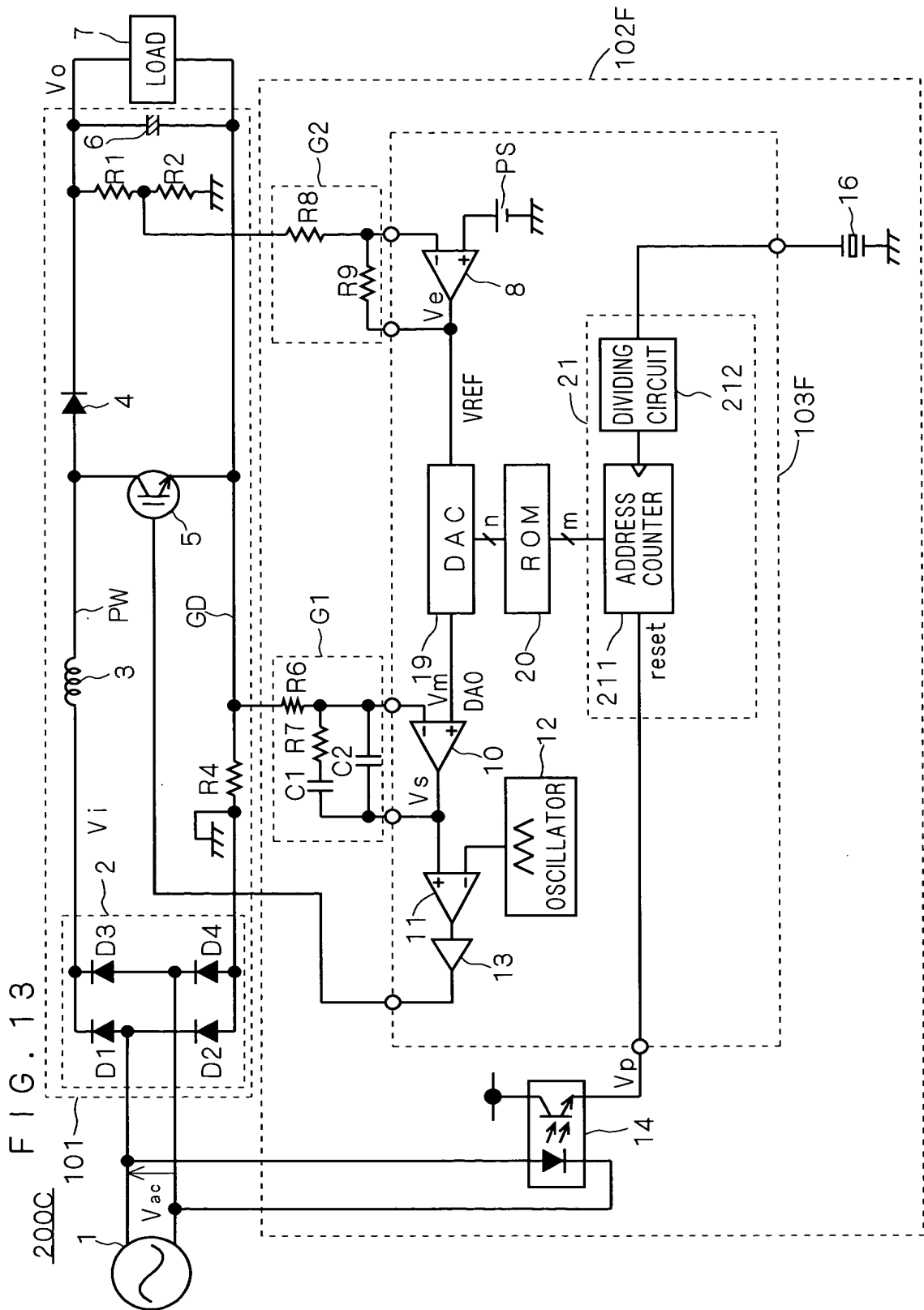




FIG. 11







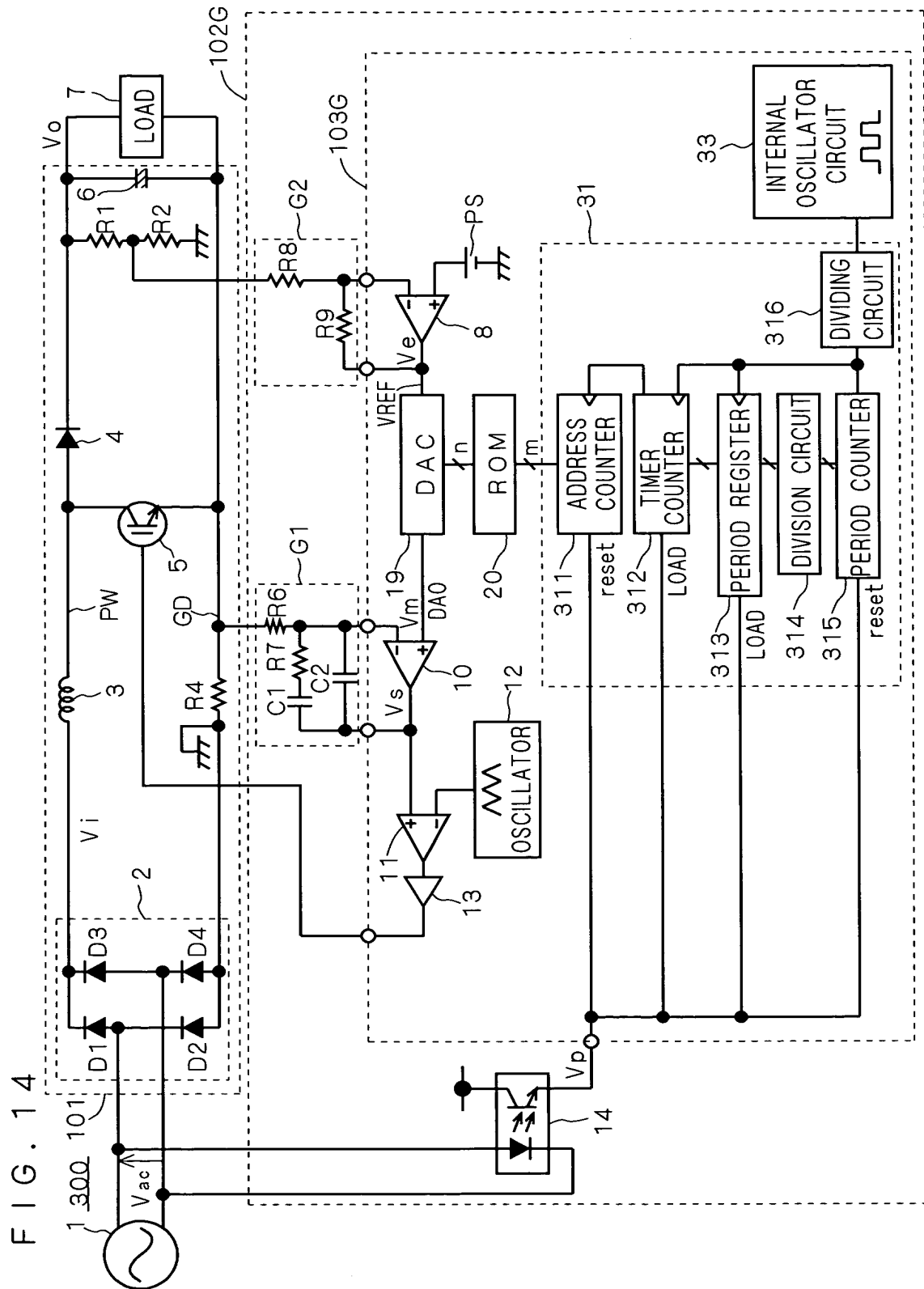
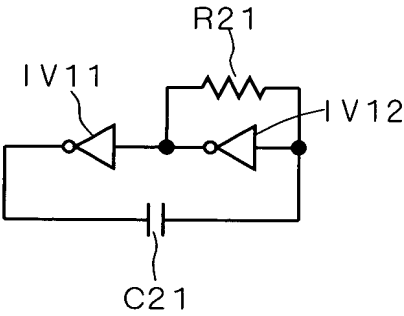


FIG. 15

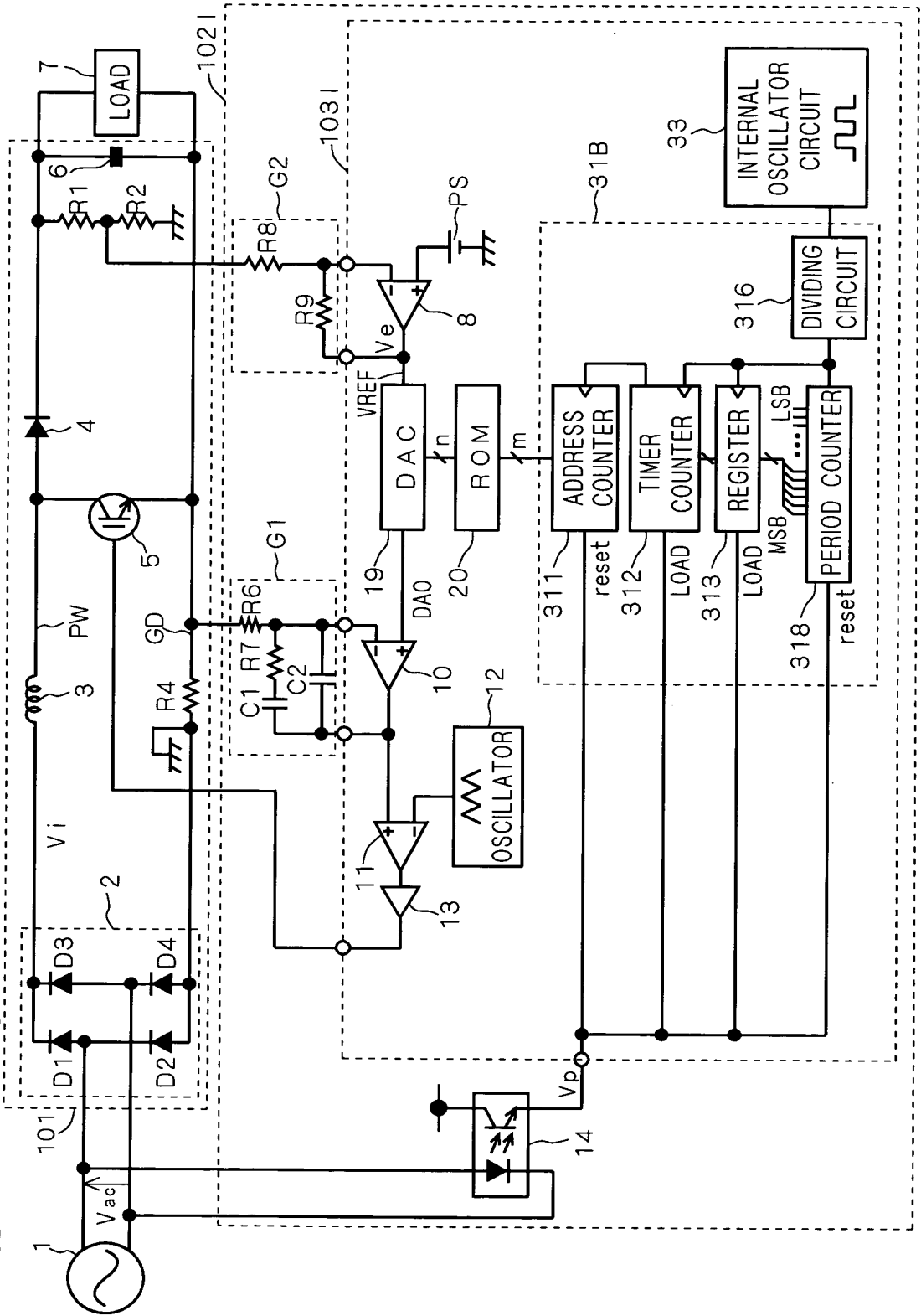




F I G . 1 7

| ROM ADDRESS | K 1 ' |
|-------------|-------|
| 0           | 0     |
| 1           | 0     |
| 2           | 1     |
| 3           | 1     |
| 4           | 2     |
| 5           | 2     |
| 6           | 3     |
| 7           | 3     |
| ...         | ...   |

FIG. 18.





F I G . 1 9

|                   | BINARY |   |   |   |     |   |   |   | DECIMAL |
|-------------------|--------|---|---|---|-----|---|---|---|---------|
|                   | MSB    |   |   |   | LSB |   |   |   |         |
| ORIGINAL DATA     | 0      | 1 | 1 | 1 | 0   | 0 | 0 | 0 | → 112   |
| 1-BIT RIGHT-SHIFT | 0      | 0 | 1 | 1 | 1   | 0 | 0 | 0 | → 56    |
| 2-BIT RIGHT-SHIFT | 0      | 0 | 0 | 1 | 1   | 1 | 0 | 0 | → 28    |